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Octal Bus Transceivers (with 3-state outputs)



ADE-205-475 (Z) 1st. Edition Sep. 2000

Description

Each device has an active low enable input \overline{G} and a direction control input, DIR. When DIR is high, data flows from the A inputs to the B outputs. When DIR is low, data flows from the B inputs to the A outputs. The HD74HC245 transfers true data from one bus to the other. This device does not have schmitt trigger inputs.

Features

High Speed Operation: t_{pd} = 8 ns typ (C_L = 50 pF)
 High Output Current: Fanout of 15 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2$ to 6 V

• Low Input Current: 1 μA max

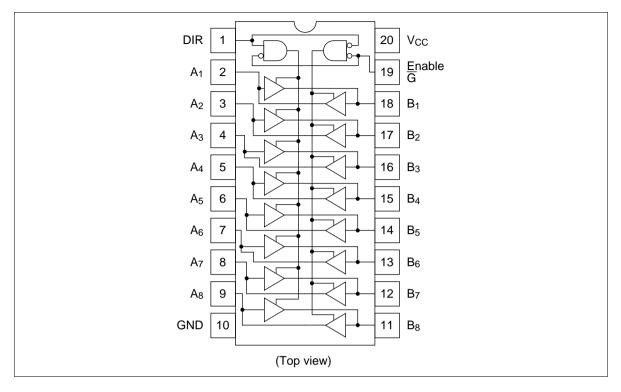
• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

Function Table

Enable G	Direction Control DIR	Operation			
L	L	B data to A bus			
L	Н	A data to B bus			
Н	X	Isolation			

H : high level
L : low level
X : irrelevant

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Rating	Unit	
Supply voltage range	V _{cc}	-0.5 to +7.0	V	
Input voltage	V_{IN}	-0.5 to $V_{cc} + 0.5$	V	
Output voltage	V _{out}	-0.5 to $V_{cc} + 0.5$	V	
DC current drain per pin	I _{OUT}	±35	mA	
DC current drain per V _{CC} , GND	$I_{\rm CC},I_{\rm GND}$	±75	mA	
DC input diode current	I _{IK}	±20	mA	
DC output diode current	I _{OK}	±20	mA	
Power dissipation per package	P _T	500	mW	
Storage temperature	Tstg	-65 to +150	°C	

DC Characteristics

			Ta = 25°C		+85°C					
Item	Symbol	V_{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	ns
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_	_		
		6.0	4.2	_	_	4.2	_	_		
	V _{IL}	2.0	_	_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35	_		
		6.0	_	_	1.8	_	1.8	_		
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4	_	_		
		6.0	5.9	6.0	_	5.9		_		
		4.5	4.18	_	_	4.13	_	_		I _{OH} = -6 mA
		6.0	5.68	_	_	5.63	_	_		$I_{OH} = -7.8 \text{ mA}$
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} or V_{IL}$	I_{OL} = 20 μA
		4.5	_	0.0	0.1	_	0.1	_		
		6.0	_	0.0	0.1	_	0.1	_		
		4.5	_	_	0.26	_	0.33	_		I _{OL} = 6 mA
		6.0	_	_	0.26	_	0.33	_		I _{OL} = 7.8 mA
Off-state output current	I _{oz}	6.0	_	_	±0.5	_	±5.0	μА	$Vin = V_{IH} \text{ or } V_{IL},$ $Vout = V_{CC} \text{ or } G$	SND
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V _{CC} or GN	ID
Quiescent supply current	I _{cc}	6.0	_	_	4.0	_	40	μА	$Vin = V_{CC}$ or GN	ID, lout = $0 \mu A$

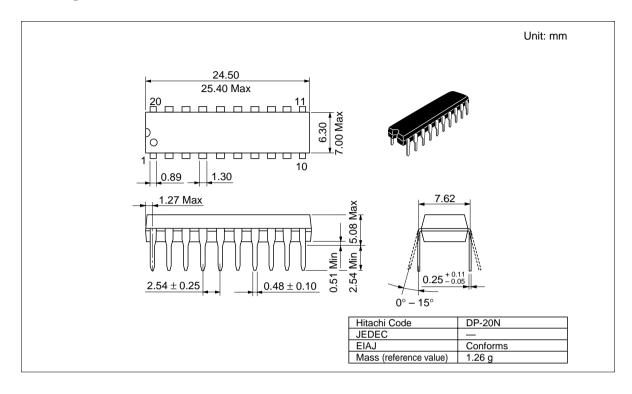
Ta = -40 to

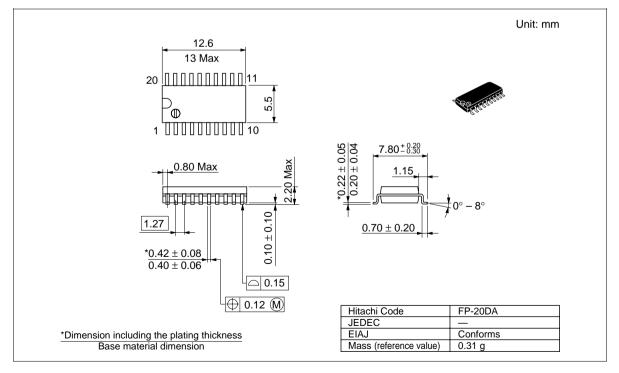
AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

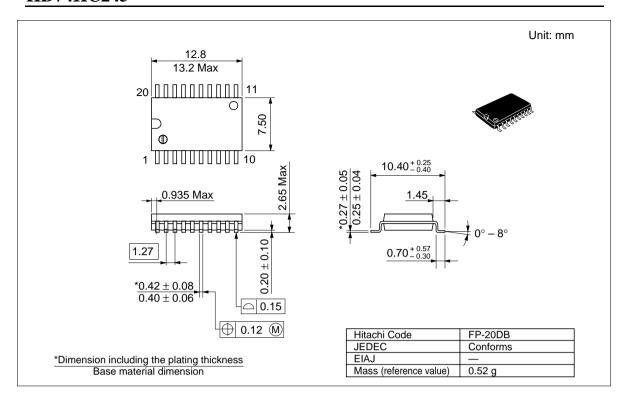
Ta = -40 to $Ta = 25^{\circ}C$ +85°C

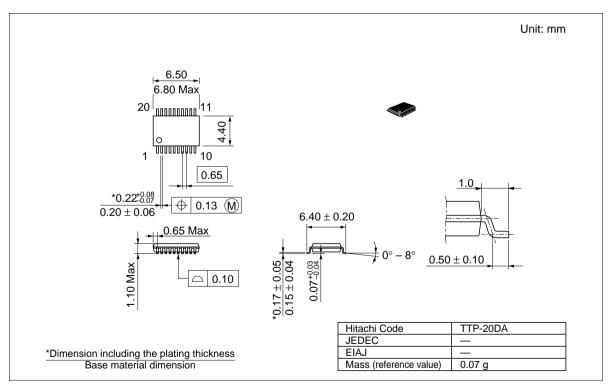
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	_	90	_	115	ns	
time		4.5	_	8	18	_	23		
		6.0	_	_	15	_	20		
	t _{PHL}	2.0	_	_	90	_	115	ns	_
		4.5		8	18	_	23		
		6.0	_	_	15	_	20		
Output enable	t _{zL}	2.0	_	_	150	_	190	ns	
time		4.5	_	16	30	_	38	=	
		6.0	_	_	26	_	32	_	
	t _{zH}	2.0	_	_	150	_	190	ns	_
		4.5	_	12	30	_	38	=	
		6.0	_	_	26	_	32	_	
Output disable	t _{LZ}	2.0	_	_	150	_	190	ns	
time		4.5	_	17	30	_	38	=	
		6.0	_	_	26	_	32	=	
	t _{HZ}	2.0	_	_	150	_	190	ns	_
		4.5	_	18	30	_	38	=	
		6.0	_	_	26	_	32	_	
Output rise/fall	t _{TLH}	2.0	_	_	60	_	75	ns	
time	t_{THL}	4.5	_	4	12	_	15	_	
		6.0	_	_	10	_	13	_	
Input capacitance	Cin	_	_	5	10	_	10	pF	

Package Dimensions









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